

CoreadDvices  
承芯微电子



# CD2902

Quad, high-voltage operational amplifier

Version: Rev 1.0.0 Date: 2025-6-3

## Features ■■

- Internally frequency compensated for unity gain
- Large DC voltage gain: 100dB
- Wide power supply range: 3V ~ 32V (or  $\pm 1.5V \sim \pm 16V$ )
- Input common-mode voltage range includes ground
- Large output voltage swing: 0V DC to VCC-1.5V DC
- Power drain suitable for battery operation
- Low input offset voltage and offset current
- Differential input voltage range equal to the power supply

## Application ■■

- Walkie-Talkie
- Battery Management Solution
- Transducer Amplifiers
- Summing Amplifiers
- Multivibrators
- Oscillators
- Portable Systems

## Description ■■

The CD29022902 contains four independent high gain operational amplifiers with internal frequency compensation. The four op-amps operate over a wide voltage range from a single power supply. Also use a split power supply. The device has low power supply current drain, regardless of the power supply voltage. The low power drain also makes the CD29022902 a good choice for battery operation.

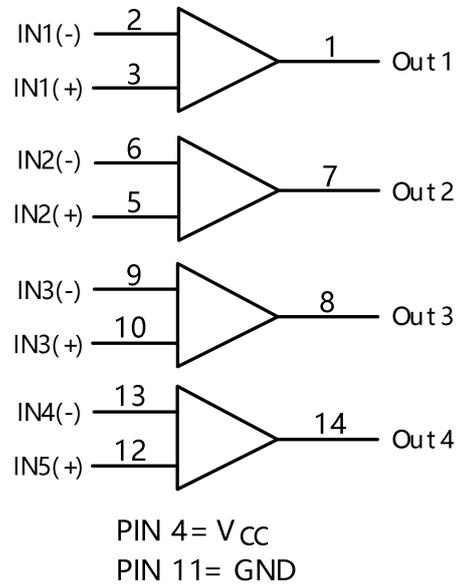
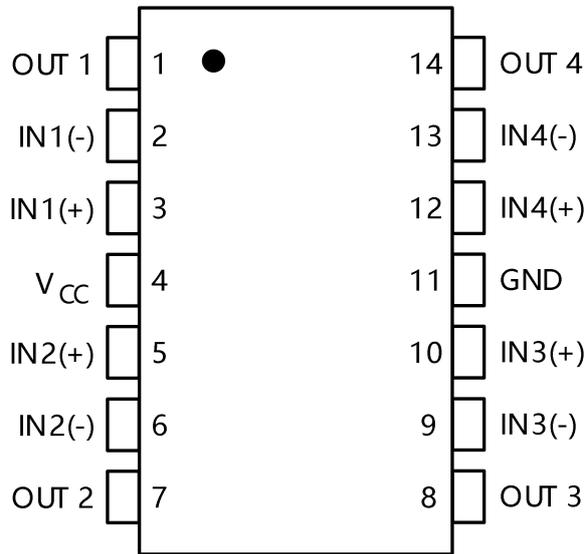
When your project calls for a traditional op-amp function, now you can streamline your design with a simple single power supply. Use ordinary +5V DC common to practically any digital system or personal computer application, without requiring an extra 15V power supply just to have the interface electronics you need.

The CD29022902 is a versatile, rugged workhorse with a thousand-and-one uses, from amplifying signals from a variety of transducers to dc gain blocks, or any op-amp function. The attached pages offer some recipes that will have your project cooking in no time.

## Contents

Features .....	- 1 -
Application .....	- 1 -
Description .....	- 1 -
Pin Configurations .....	- 3 -
Absolute Maximum Ratings .....	- 3 -
Recommended Operating Conditions .....	- 3 -
Electrical characteristics .....	- 4 -
Typical Characteristics .....	- 5 -
Package Outline Dimensions .....	- 6 -
Package/Ordering Information .....	- 7 -
Revision Log .....	- 8 -

## Pin Configurations



## Absolute Maximum Ratings

Symbol	Parameter	Value	Unit
$V_{CC}$	Power Supply Voltages •Single Supply •Split Supplies	32 $\pm 16$	V
$V_{IDR}$	Input Differential Voltage Range (1)	$\pm 32$	V
$V_{ICR}$	Input Common Mode Voltage Range	-0.3 to 32	V
$I_{SC}$	Output Short Circuit Duration	Continuous	
$T_J$	Junction Temperature (Plastic Packages)	150	$^{\circ}\text{C}$
$T_{stg}$	Storage Temperature (Plastic Packages)	-55 to +125	$^{\circ}\text{C}$
$I_{IN}$	Input Current, per pin (2)	50	mA
$T_L$	Lead Temperature, 1mm from Case for 10 Seconds	260	$^{\circ}\text{C}$

## Recommended Operating Conditions

Symbol	Parameter	Min	Max	Unit
$V_{CC}$	DC Supply Voltage	$\pm 2.5$ or 5.0	$\pm 15$ or 30	V
$T_A$	Operating Temperature, All Package Types	-40	+105	$^{\circ}\text{C}$

## Electrical characteristics

Symbol	Parameter	Test Conditions	Guaranteed Limit			Unit
			Min	Typ	Max	
V <sub>IO</sub>	Maximum Input Offset Voltage	V <sub>O</sub> =1.4V V <sub>CC</sub> =5.0-30V; R <sub>S</sub> =0			9.0	mV
		Ω			5.0*	
ΔV <sub>IO</sub> /ΔT	Input Offset Voltage Drift	R <sub>S</sub> =0Ω, V <sub>CC</sub> =30V		7.0		μV/°C
I <sub>IO</sub>	Maximum Input Offset Current	V <sub>CC</sub> =5.0V			150	nA
					50*	
ΔI <sub>IO</sub> /ΔT	Input Offset Current Drift	R <sub>S</sub> =0Ω, V <sub>CC</sub> =30V		10		pA/ °C
I <sub>IB</sub>	Maximum Input Bias Current	V <sub>CC</sub> = 5.0V			500	nA
					250*	
V <sub>ICR</sub>	Input Common Mode Voltage Range	V <sub>CC</sub> = 30V	0		28	V
I <sub>CC</sub>	Maximum Power Supply Current	R <sub>L</sub> =∞, V <sub>CC</sub> =30V, V <sub>O</sub> =0V			3	mA
		R <sub>L</sub> =∞, V <sub>CC</sub> =5V, V <sub>O</sub> =0V			1.2	
A <sub>VOL</sub>	Minimum Large Signal Open-Loop Voltage Gain	V <sub>CC</sub> =15V, R <sub>L</sub> ≥ 2KΩ	15			V/mV
			25*			
V <sub>OH</sub>	Minimum Output	V <sub>CC</sub> =30V, R <sub>L</sub> =2KΩ	26			V
	High-Level Voltage Swing	V <sub>CC</sub> =30V, R <sub>L</sub> =10KΩ	27			
V <sub>OL</sub>	Maximum Output Low-Level Voltage Swing	V <sub>CC</sub> =5V, R <sub>L</sub> =10KΩ			20	mV
C <sub>MR</sub>	Common Mode Rejection	V <sub>CC</sub> =30V, R <sub>S</sub> =10KΩ	65*			dB
P <sub>SR</sub>	Power Supply Rejection	V <sub>CC</sub> =30V	65*			dB
C <sub>S</sub>	Channel Separation	f=1KHz to 20KHz, V <sub>CC</sub> =30V	120			dB
I <sub>SC</sub>	Maximum Output Short Circuit to GND	V <sub>CC</sub> =5.0V			60*	mA
I <sub>source</sub>	Minimum Output Source Current	V <sub>IN+</sub> =1V, V <sub>IN-</sub> =0V, V <sub>CC</sub> =15V, V <sub>O</sub> =0V	10			mA
I <sub>sink</sub>	Minimum Output Sink Current	V <sub>IN+</sub> =0V, V <sub>IN-</sub> =1V, V <sub>CC</sub> =15V, V <sub>O</sub> =15V	5			mA
		V <sub>O</sub> =15V	10*			
		V <sub>IN+</sub> =0V, V <sub>IN-</sub> =1V,	12*			μA

		$V_{CC}=15V, V_0=0.2V$			
VIDR	Differential Input Voltage Range	All $V_{IN} \geq GND$ or V-Supply (if used)		$V_{CC}^*$	V

\*=@25°C

### Typical Characteristics

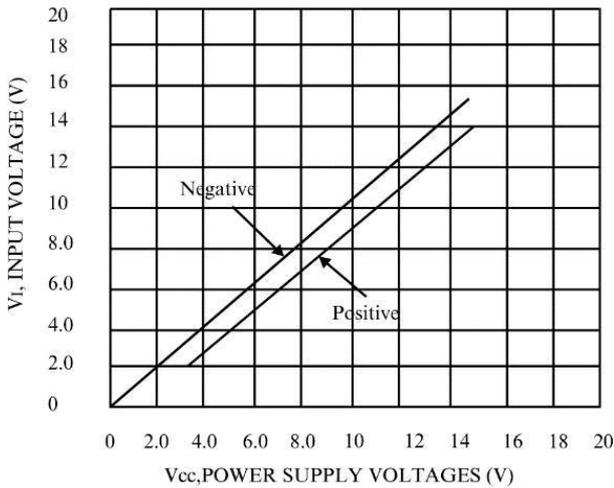
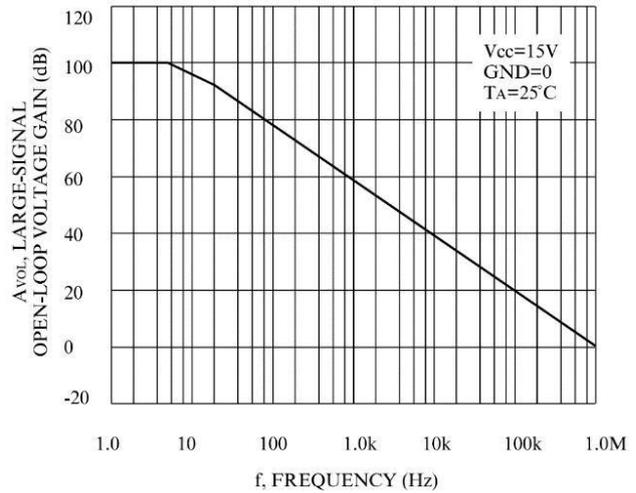


Figure 1. Input Voltage Range Figure



2. Open-Loop Frequency

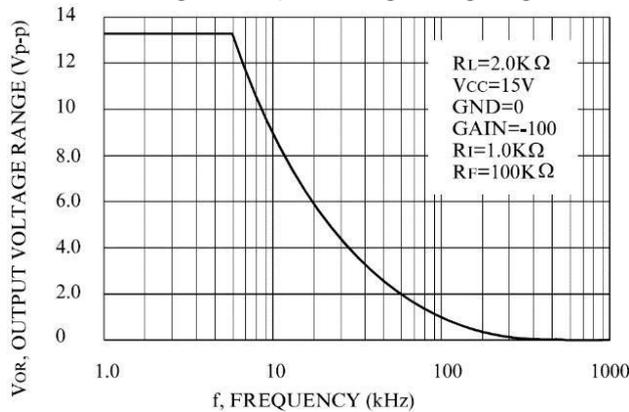


Figure 3. Large-Signal Frequency Response

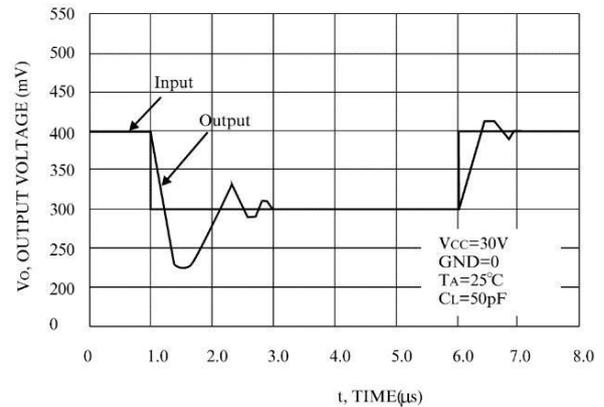


Figure 4. Small-Signal Voltage Follower Pulse Response (Noninverting)

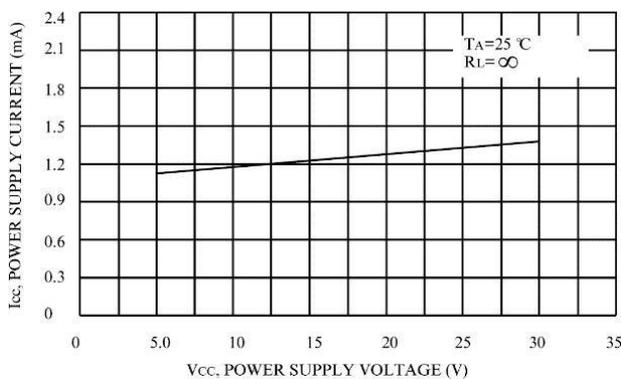


Figure 5. Power Supply Current versus Power Supply Voltage

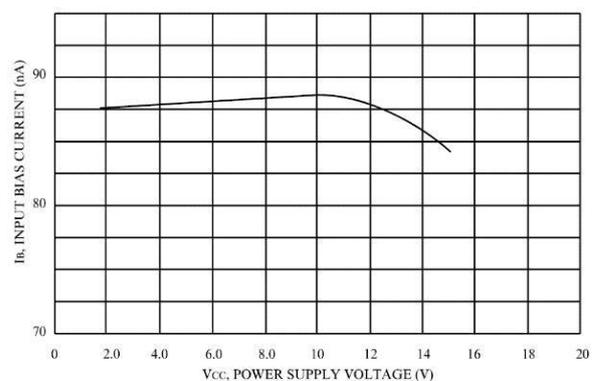
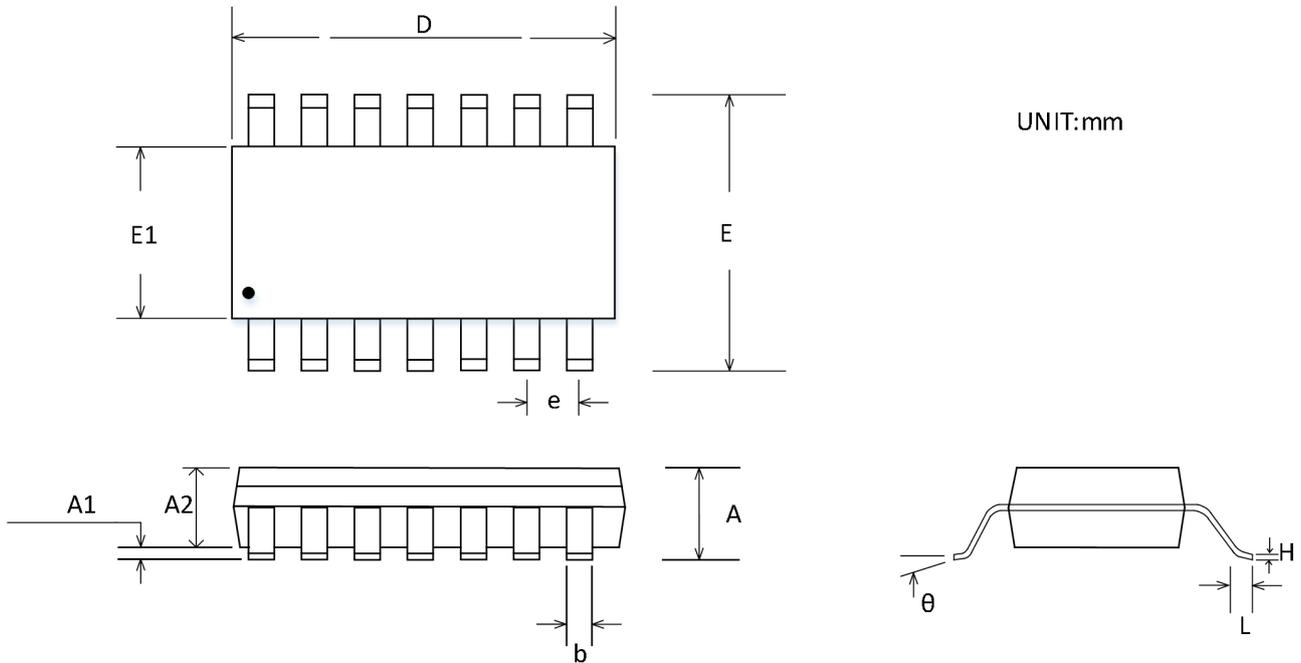


Figure 6. Input Bias Current versus Power Supply Voltage

## Package Outline Dimensions

### SOP-14



Symbol	Dimensions In Millimeters	
	Min	Max
A	1.35	1.75
A1	0.10	0.25
A2	1.25	1.50
b	0.31	0.51
D	8.55	8.75
E	5.80	6.20
E1	3.80	4.00
e	1.27BSC	
H	0.17	0.25
L	0.40	1.27
θ	0°	8°

## Package/Ordering Information

MODEL	TEMPERATURE	PACKAGE DESCRIPTION	PACKAGE OPTION
CD29022902AS14	-40°C~105°C	SOP-14	Tape and Reel, 2500
CD29022902AS14-RL	-40°C~105°C	SOP-14	Tape and Reel, 3000
CD29022902AS14-REEL	-40°C~105°C	SOP-14	Tape and Reel, 4000
CD29022902ATS14	-40°C~105°C	TSSOP-14	Tape and Reel, 2500
CD29022902ATS14-RL	-40°C~105°C	TSSOP-14	Tape and Reel, 3000
CD29022902ATS14-REEL	-40°C~105°C	TSSOP-14	Tape and Reel, 4000

## Revision Log

Version	Revision date	Change content	Reason for Change	Modified by	Reviewed By	Note
V1.0	2025.6.3	Initial version	Regular update	WW	LYL	